MCH6663



www.onsemi.com

Power MOSFET 30V, 188mΩ, 1.8A, -30V, 325mΩ, -1.5A, **Complementary Dual**

Features

• ON-Resistance Nch: $RDS(on)1=145m\Omega$ (typ)

Pch : $R_{DS}(on)1=250m\Omega$ (typ)

• 4V Drive

• Complementary N-Channel and P-Channel MOSFET

• Pb-Free, Halogen Free and RoHS Compliance

VDSS R_DS(on) Max ID Max 188 mΩ@ 10V N-Ch 343 mΩ@ 4.5V 1.8A 30V 378 mΩ@ 4V $325 \text{ m}\Omega@-10V$ P-Ch -1.5A 555 mΩ@ -4.5V -30V 641 mΩ@ -4V

Specifications

Absolute Maximum Ratings at Ta = 25°C

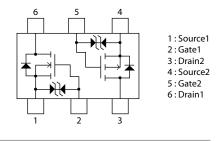
Parameter	Symbol	N-channel	P-channel	Unit
Drain to Source Voltage	V _{DSS}	30	-30	V
Gate to Source Voltage	V _{GSS}	±20	±20	V
Drain Current (DC)	ID	1.8	-1.5	Α
Drain Current (Pulse) PW≤10μs, duty cycle≤1%	I _{DP}	7.2	-6	Α
Power Dissipation When mounted on ceramic substrate (900mm²×0.8mm) 1unit	PD	0.8		W
Junction Temperature	Tj	150		°C
Storage Temperature	Tstg	-55 to +150		°C

This product is designed to "ESD immunity < 200V*", so please take care when handling.

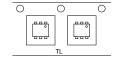
Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction to Ambient When mounted on ceramic substrate (900mm²x0.8mm) 1unit	R _{θJA}	156.25	°C/W

Electrical Connection N-Channel and P-Channel



Packing Type: TL Marking





Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

ORDERING INFORMATION

See detailed ordering and shipping information on page 7 of this data sheet.

MCH6663

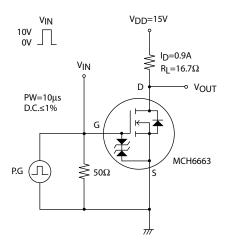
Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions		Value		
Parameter Symbol Conditions		min	typ	max	Unit	
[N-channel]						
Drain to Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _{GS} =0V	30			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =30V, V _{GS} =0V			1	μΑ
Gate to Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0V			±10	μΑ
Gate Threshold Voltage	V _{GS} (th)	V _{DS} =10V, I _D =1mA	1.2		2.6	V
Forward Transconductance	gFS .	V _{DS} =10V, I _D =0.9A		1.1		S
Static Drain to Source On-State Resistance	R _{DS} (on)1	I _D =0.9A, V _{GS} =10V		145	188	mΩ
	R _{DS} (on)2	I _D =0.5A, V _{GS} =4.5V		245	343	mΩ
	R _{DS} (on)3	I _D =0.5A, V _{GS} =4V		270	378	mΩ
Input Capacitance	Ciss			88		pF
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		19		pF
Reverse Transfer Capacitance	Crss			11		pF
Turn-ON Delay Time	t _d (on)			3.4		ns
Rise Time	t _r	0		3.6		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		10.5		ns
Fall Time	t _f	7		4.0		ns
Total Gate Charge	Qg			2.0		nC
Gate to Source Charge	Qgs	V _{DS} =15V, V _{GS} =10V, I _D =1.8A		0.33		nC
Gate to Drain "Miller" Charge	Qgd	7		0.29		nC
Forward Diode Voltage	V _{SD}	I _S =1.8A, V _{GS} =0V		0.86	1.2	V
[P-channel]		•				
Drain to Source Breakdown Voltage	V(BR)DSS	I _D =-1mA, V _{GS} =0V	-30			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =-30V, V _{GS} =0V			-1	μА
Gate to Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0V			±10	μА
Gate Threshold Voltage	V _{GS} (th)	V _{DS} =-10V, I _D =-1mA	-1.2		-2.6	V
Forward Transconductance	9FS	V _{DS} =-10V, I _D =-0.8A		1.3		S
	R _{DS} (on)1	I _D =-0.8A, V _{GS} =-10V		250	325	mΩ
Static Drain to Source On-State Resistance	R _{DS} (on)2	I _D =-0.4A, V _{GS} =-4.5V		397	555	mΩ
	R _{DS} (on)3	I _D =-0.4A, V _{GS} =-4V		458	641	mΩ
Input Capacitance	Ciss			82		pF
Output Capacitance	Coss	V _{DS} =–10V, f=1MHz		22		pF
Reverse Transfer Capacitance	Crss			16		pF
Turn-ON Delay Time	t _d (on)			4.0		ns
Rise Time	t _r	Conservation Total Circ. V		3.3		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		12		ns
Fall Time	tf			5.4		ns
Total Gate Charge	Qg			2.2		nC
Gate to Source Charge	Qgs	V _{DS} =-15V, V _{GS} =-10V, I _D =-1.5A		0.36		nC
Gate to Drain "Miller" Charge	Qgd	7		0.49		nC

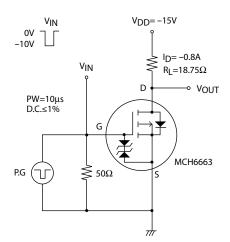
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

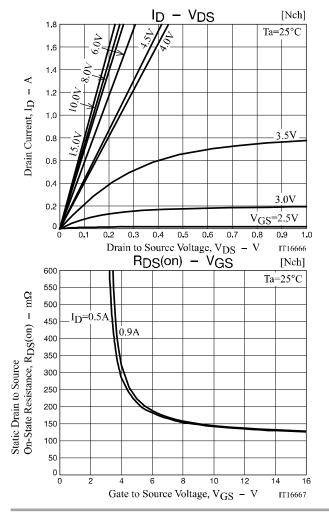
Switching Time Test Circuit

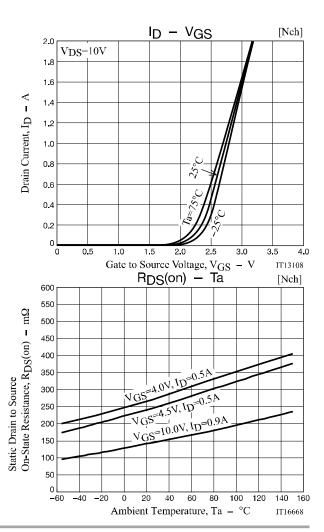
[N-channel]

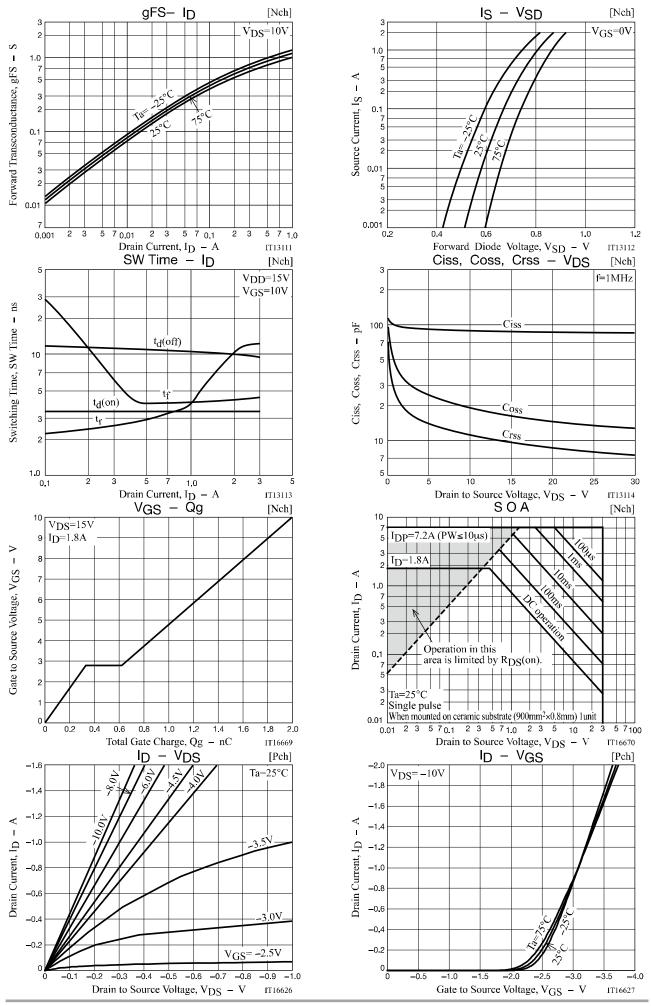


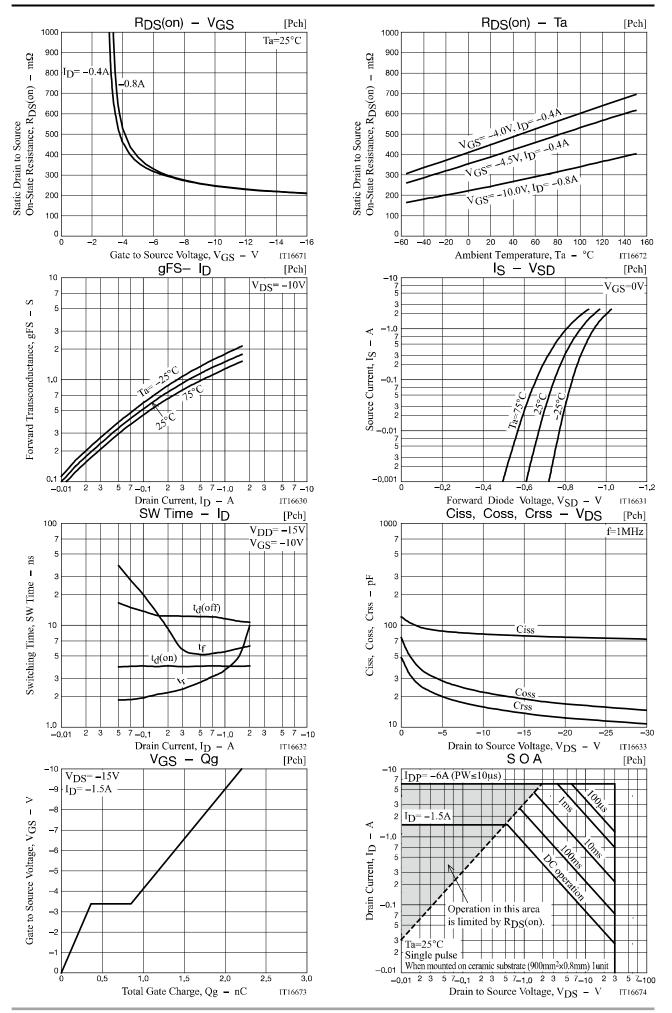
[P-channel]



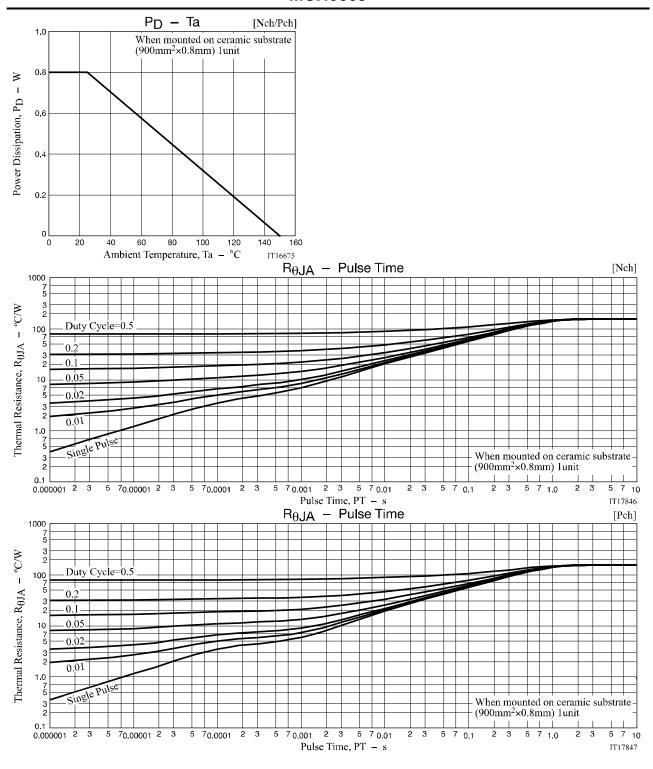








MCH6663



Package Dimensions

MCH6663-TL-H / MCH6663-TL-W

MCPH6

CASE 419AS ISSUE O

unit: mm

1: Source1

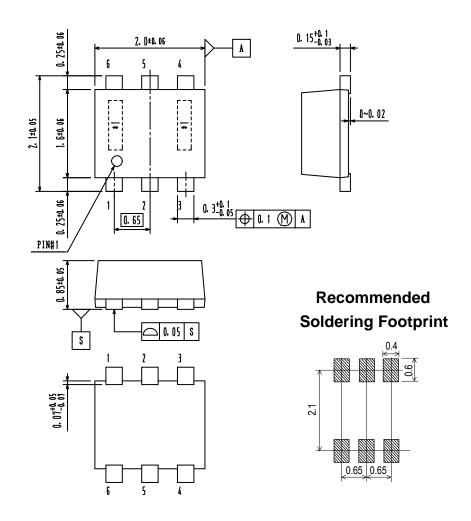
2: Gate1

3: Drain2

4 : Source2

5: Gate 2

6: Drain1



ORDERING INFORMATION

Device	Package	Shipping	Note	
MCH6663-TL-H	MCPH6	3,000 pcs. / Tape & Reel	Pb-Free and Halogen Free	
MCH6663-TL-W	SC-88,SC-70-6,SOT-363	5,000 pcs. / Tape & Neel		

Note on usage: Since the MCH6663 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries. SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent re

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ON Semiconductor:

MCH6663-TL-H MCH6663-TL-W