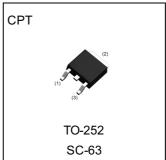


Power Transistor(80V,1A)

Parameter	Value
V_{CEO}	80V
I _C	1A

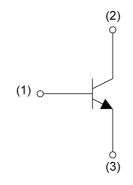
Outline



Features

- 1)High V_{CEO},VCEO=80V
- 2)High I_C,I_C=1A(DC)
- 3)Good hFE linearity
- 4)Low V_{CE(sat)}
- 5)Complements the 2SB1181

•Inner circuit



- (1) Base
- (2) Collector
- (3) Emitter

Application

Low frequency power amplifier

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
2SD1733	CPT	6595	TL	330	16	2500	D1733

● Absolute maximum ratings (T_a = 25°C)

Parameter	Symbol	Values	Unit
Collector-base voltage	V_{CBO}	120	V
Collector-emitter voltage	V _{CEO}	80	V
Emitter-base voltage	V _{EBO}	5	V
Collector current	I _C	1	Α
Device discipation	P _D *1	1	W
Power dissipation	P _D *2	10	W
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

• Electrical characteristics $(T_a = 25^{\circ}C)$

Davameter	Cumphal	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Collector-base breakdown voltage	BV _{CBO}	I _C = 50μA	120	1	1	V
Collector-emitter breakdown voltage	BV _{CEO}	I _C = 1mA	80	ı	ı	V
Emitter-base breakdown voltage	BV _{EBO}	I _E = 50μA	5	1	1	٧
Collector cut-off current	I _{CBO}	V _{CB} = 100V	1	1	1.0	μA
Emitter cut-off current	I _{EBO}	V _{EB} = 4V	ı	ı	1.0	μA
Collector-emitter saturation voltage	V _{CE(sat)}	I _C = 500mA, I _B = 20mA	ı	ı	400	mV
DC current gain	h _{FE}	V _{CE} = 3V, I _C = 500mA	82	-	390	-
Transition frequency	f⊤	V _{CE} = 10V, I _E = -50mA, f = 100MHz	1	100	1	MHz
Output capacitance	C _{ob}	V _{CB} = 10V, I _E = 0A, f = 1MHz	-	20	-	pF

hFE values are calssified as follows:

rank	Р	Q	R	-	-
h _{FE}	82-180	120-270	180-390	-	-

^{*1} Ta=25℃

^{*2} Tc=25℃

● Electrical characteristic curves(T_a = 25°C)

Fig.1 Ground Emitter Propagation Characteristics

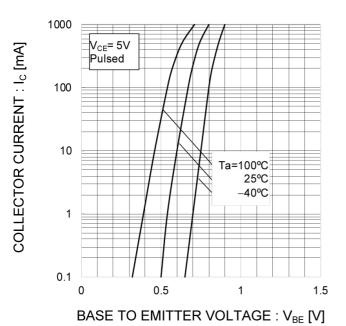
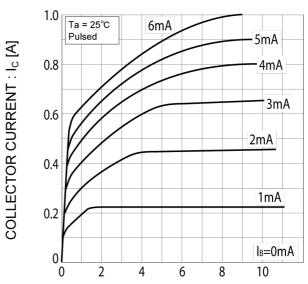


Fig.2 Typical Output Characteristics



COLLECTOR TO EMITTER VOLTAGE: V_{CE} [V]

Fig.3 DC Current Gain vs. Collector Current (I)

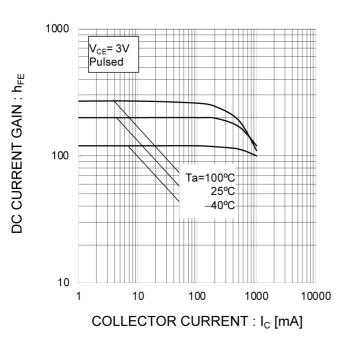
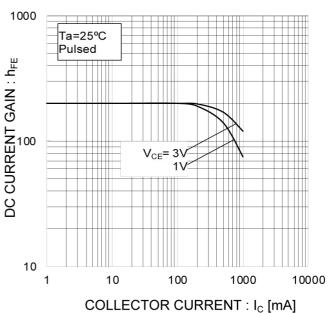


Fig.4 DC Current Gain vs. Collector Current (II)



● Electrical characteristic curves(T_a = 25°C)

Fig.5 Collector-Emitter Saturation Voltage vs. Collector Current (I)

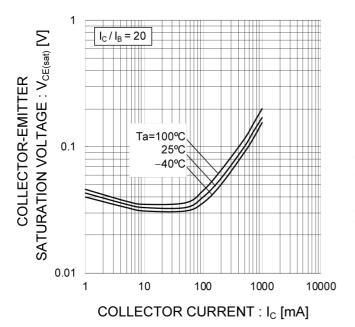


Fig.6 Collector-Emitter Saturation Voltage vs. Collector Current (II)

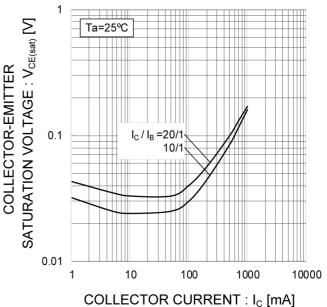


Fig.7 Base-Emitter Saturation Voltage vs. Collector Current

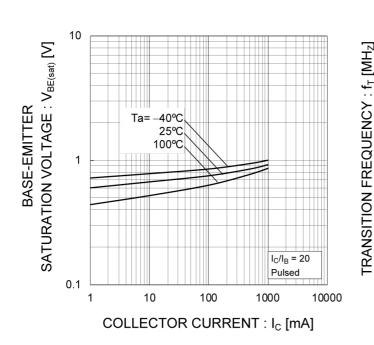
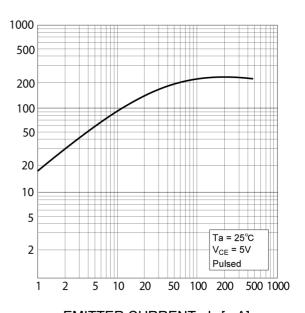


Fig.8 Gain Bandwidth Product vs. Emitter Current

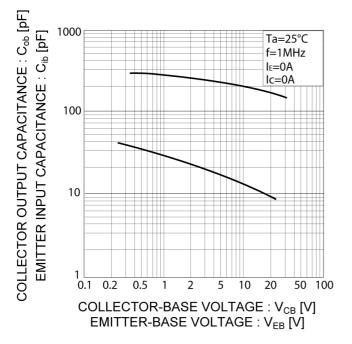


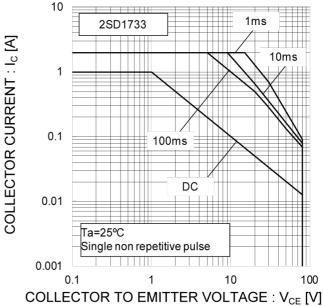
EMITTER CURRENT : IE [mA]

● Electrical characteristic curves(T_a = 25°C)

Fig.9 Emitter input capacitance
vs. Emitter-Base Voltage
Collector output capacitance
vs. Collector-Base Voltage

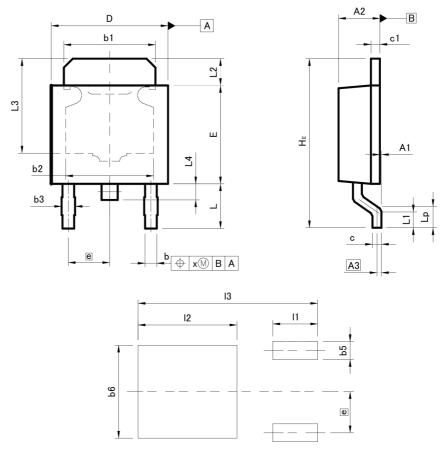
Fig.10 Safe Operating Area





Dimensions

CPT



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
A1	0.00	0.15	0.000	0.006	
A2	2.20	2.50	0.087	0.098	
A3	0.:	25	0.010		
b	0.55	0.75	0.022	0.030	
b1	5.00	5.30	0.197	0.209	
b2	5.	00	0.1	97	
b3	0.	75	0.0	30	
С	0.40	0.60	0.016	0.024	
c1	0.40	0.60	0.016	0.024	
D	6.30	6.70	0.248	0.264	
Е	5.40	5.80	0.213	0.228	
е	2.	30	0.091		
HE	9.00	10.00	0.354	0.394	
L	2.20	2.80	0.087	0.110	
L1	0.80	1.40	0.031	0.055	
L2	1.20	1.80	0.047	0.071	
L3	5.	30	0.209		
L4	0.90		0.035		
Lp	1.00	1.60	0.039	0.063	
X	_	0.25	_	0.010	

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
b5	-	1.00	-	0.04	
b6	-	5.20	-	0.205	
l1	-	2.50	_	0.098	
12	_	5.50	_	0.217	
13	_	10.00	_	0.394	

Dimension in mm/inches



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