Power MOSFET 30V, $50m\Omega$, 4A, Single N-Channel

This Power MOSFET is produced using ON Semiconductor's trench technology, which is specifically designed to minimize gate charge and low on resistance. This device is suitable for applications with low gate charge driving or low on resistance requirements.

Features

- Low On-Resistance
- High Speed Switching
- 1.8V drive
- ESD Diode-Protected Gate
- Pb-Free, Halogen Free and RoHS compliance

Typical Applications

• DC/DC Converter

SPECIFICATIONS

ABSOLUTE MAXIMUM RATING at Ta = 25°C (Note 1)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	VDSS	30	٧
Gate to Source Voltage	VGSS	±12	٧
Drain Current (DC)	ID	4	Α
Drain Current (Pulse) PW ≤ 10µs, duty cycle ≤ 1%	IDP	16	Α
Power Dissipation When mounted on ceramic substrate (900mm²×0.8mm)	PD	1	W
Junction Temperature	Tj	150	°C
Storage Temperature	Tstg	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit			
Junction to Ambient When mounted on ceramic substrate (900mm ² × 0.8mm)	$R_{\theta JA}$	125	°C/W			
(900mm × 0.8mm)						

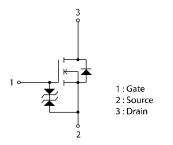


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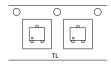
www.onsemi.com

VDSS	R _{DS} (on) Max	ID Max	
	50mΩ@ 4.5V		
30V	72mΩ@ 2.5V	4A	
	130mΩ@ 1.8V		

ELECTRICAL CONNECTION N-Channel



PACKING TYPE: TL MARKING





ORDERING INFORMATION

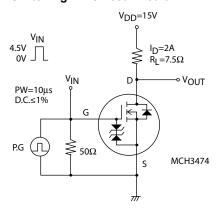
See detailed ordering and shipping information on page 5 of this data sheet.

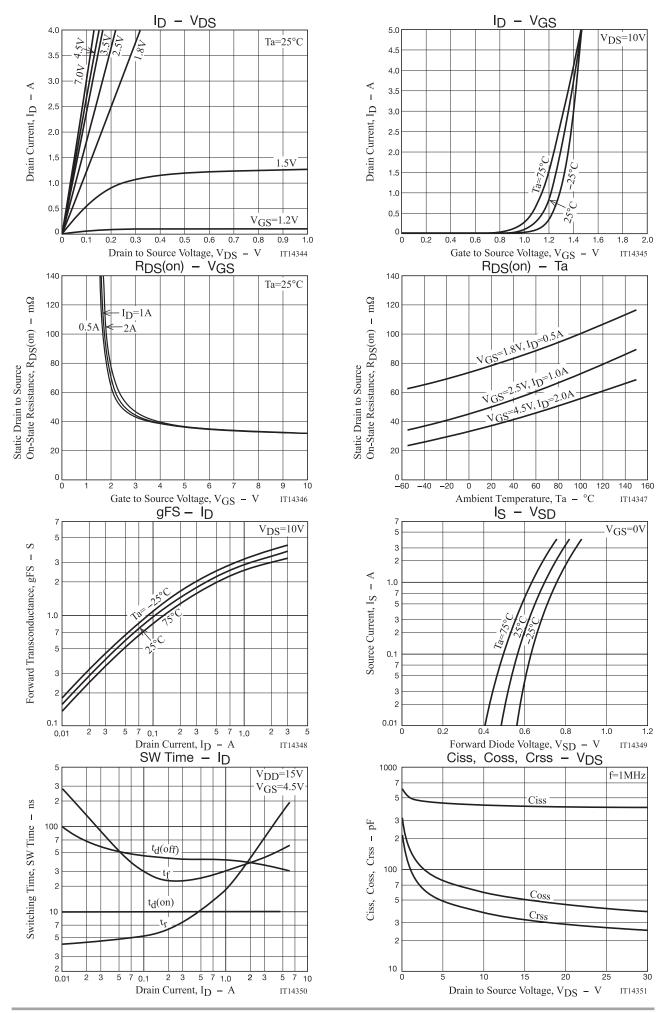
ELECTRICAL CHARACTERISTICS at Ta = 25°C (Note 2)

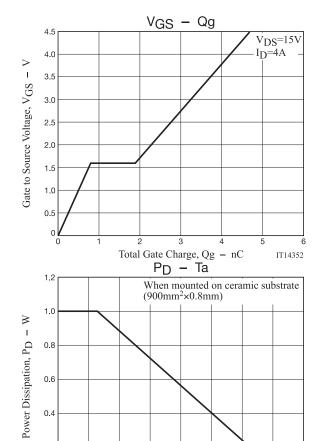
Parameter	Symbol	Conditions	Value			Unit
Farameter	Syllibol	Conditions	min	typ	max	Offic
Drain to Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	30			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =30V, V _{GS} =0V			1	μΑ
Gate to Source Leakage Current	IGSS	V _{GS} =±8V, V _{DS} =0V			±10	μΑ
Gate Threshold Voltage	VGS(th)	V _{DS} =10V, I _D =1mA	0.4		1.3	V
Forward Transconductance	gFS .	V _{DS} =10V, I _D =2A	2.0	3.4		S
	R _{DS} (on)1	I _D =2A, V _G S=4.5V		38	50	mΩ
Static Drain to Source On-State Resistance	R _{DS} (on)2	I _D =1A, V _G S=2.5V		51	72	mΩ
Resistance	R _{DS} (on)3	I _D =0.5A, V _{GS} =1.8V		80	130	mΩ
Input Capacitance	Ciss			430		pF
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		59		pF
Reverse Transfer Capacitance	Crss			38		pF
Turn-ON Delay Time	t _d (on)			10		ns
Rise Time	t _r	On a supplified Total Cincell		41		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		36		ns
Fall Time	tf			37		ns
Total Gate Charge	Qg			4.7		nC
Gate to Source Charge	Qgs	V _{DS} =15V, V _{GS} =4.5V, I _D =4A		0.8		nC
Gate to Drain "Miller" Charge	Qgd			1.1		nC
Forward Diode Voltage	V _{SD}	IS=4A, VGS=0V		0.82	1.2	V

Note 2 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Switching Time Test Circuit



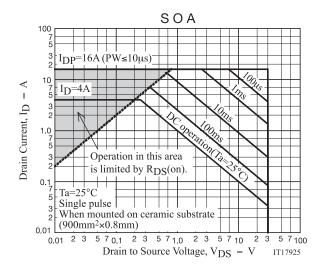


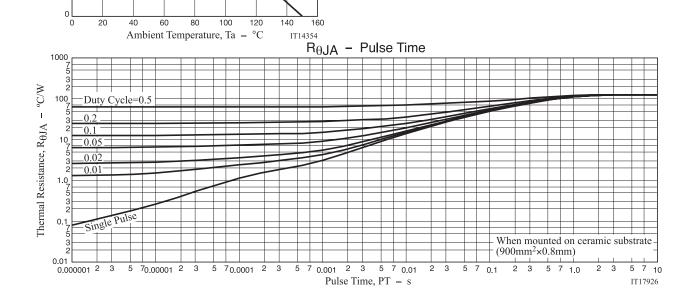


0.6

0.4

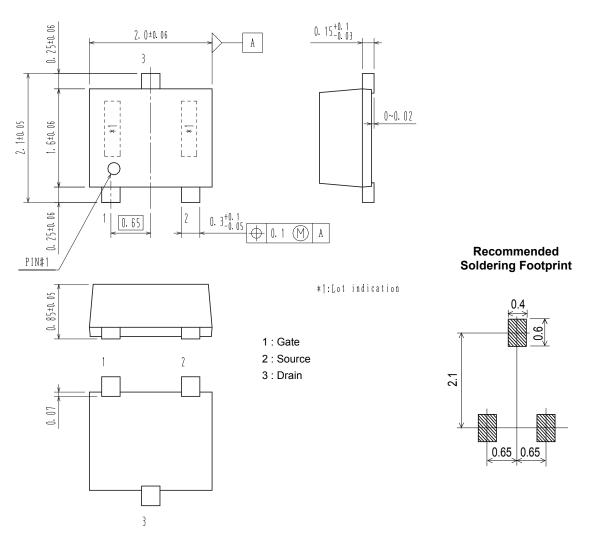
0.2





PACKAGE DIMENSIONS

unit: mm SC-70FL/MCPH3 CASE 419AQ ISSUE O



ORDERING INFORMATION

Device	Marking	Package	Shipping (Qty / Packing)	
MCH3474-TL-H	FF	SC-70FL / MCPH3	3,000 / Tape & Reel	
MCH3474-TL-W	FF	(Pb-Free / Halogen Free)		

[†] For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF

Note on usage: Since the MCH3474 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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MCH3474-TL-W