DM74LS04 Hex Inverting Gates

# SEMICONDUCTOR

FAIRCHILD

### DM74LS04 Hex Inverting Gates

#### **General Description**

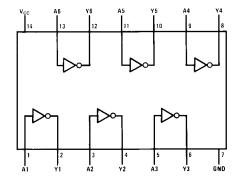
This device contains six independent gates each of which performs the logic INVERT function.

#### **Ordering Code:**

Order Number	Order Number Package Number Package Description				
DM74LS04M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow			
DM74LS04SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide			
DM74LS04N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide			

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

#### **Connection Diagram**



#### Function Table

 $\mathbf{Y} = \overline{\mathbf{A}}$ 

Input	Output		
A	Y		
L	Н		
н	L		

H = HIGH Logic Level L = LOW Logic Level



#### © 2000 Fairchild Semiconductor Corporation DS006345

#### Absolute Maximum Ratings(Note 1)

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature Range	$0^{\circ}C$ to $+70^{\circ}C$
Storage Temperature Range	-65°C to +150°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

#### **Recommended Operating Conditions**

Symbol	Parameter	Min	Nom	Max	Units
V <sub>CC</sub>	Supply Voltage	4.75	5	5.25	V
V <sub>IH</sub>	HIGH Level Input Voltage	2			V
V <sub>IL</sub>	LOW Level Input Voltage			0.8	V
он	HIGH Level Output Current			-0.4	mA
lol	LOW Level Output Current			8	mA
T <sub>A</sub>	Free Air Operating Temperature	0		70	°C

#### **Electrical Characteristics**

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$			-1.5	V
V <sub>OH</sub>	HIGH Level Output Voltage	$V_{CC} = Min, I_{OH} = Max,$ $V_{IL} = Max$	2.7	3.4		V
V <sub>OL</sub>	LOW Level Output Voltage	$V_{CC} = Min, I_{OL} = Max,$ $V_{IH} = Min$		0.35	0.5	v
		$I_{OL} = 4 \text{ mA}, V_{CC} = Min$		0.25	0.4	
lı	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$			0.1	mA
IIH	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.7V$			20	μA
IIL	LOW Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-0.36	mA
l <sub>os</sub>	Short Circuit Output Current	V <sub>CC</sub> = Max (Note 3)	-20		-100	mA
I <sub>ССН</sub>	Supply Current with Outputs HIGH	V <sub>CC</sub> = Max		1.2	2.4	mA
I <sub>CCL</sub>	Supply Current with Outputs LOW	V <sub>CC</sub> = Max		3.6	6.6	mA

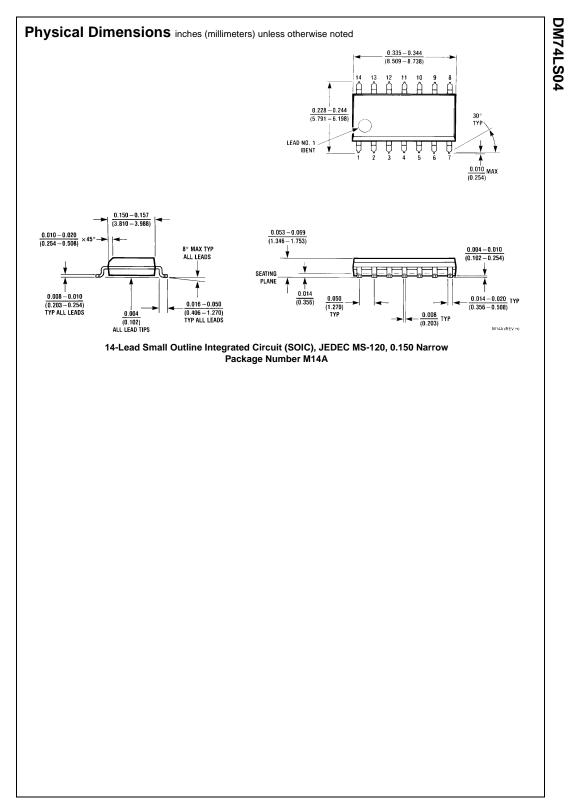
Note 2: All typicals are at  $V_{CC} = 5V$ ,  $T_A = 25^{\circ}C$ .

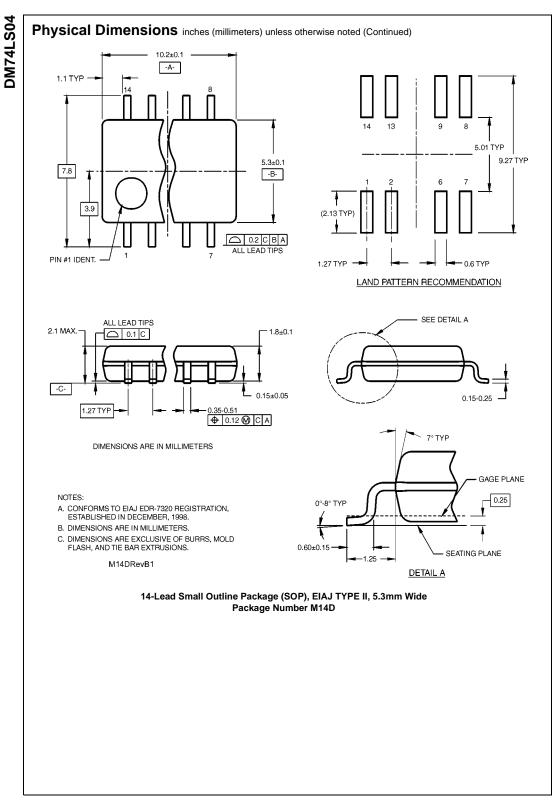
Note 3: Not more than one output should be shorted at a time, and the duration should not exceed one second.

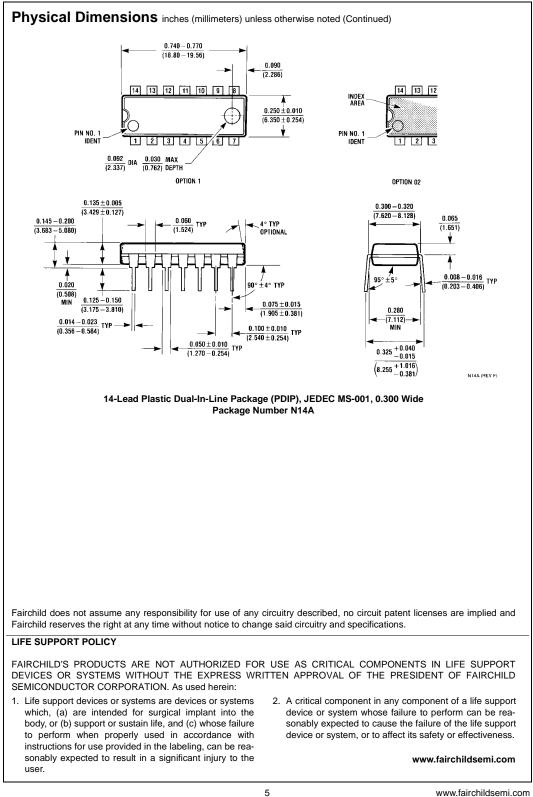
#### **Switching Characteristics**

at  $V_{CC}=5V$  and  $T_A=25^\circ C$ 

	Parameter					
Symbol		C <sub>L</sub> = 15 pF		C <sub>L</sub> = 50 pF		Units
		Min	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay Time LOW-to-HIGH Level Output	3	10	4	15	ns
t <sub>PHL</sub>	Propagation Delay Time HIGH-to-LOW Level Output	3	10	4	15	ns







DM74LS04 Hex Inverting Gates

## **Mouser Electronics**

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Onsemi: DM74LS04M DM74LS04N