Quad TTL-to-MECL Translator With TTL Strobe Input

The MC10H124 is a quad translator for interfacing data and control signals between a saturated logic section and the MECL section of digital systems. The 10H part is a functional/pinout duplication of the standard MECL 10K family part, with 100% improvement in propagation delay, and no increase in power–supply current.

- Propagation Delay, 1.5 ns Typical
- Improved Noise Margin 150 mV (Over Operating Voltage and Temperature Range)
- Voltage Compensated
- MECL 10K–Compatible

MAXIMUM RATINGS

Characteristic	Symbol	Rating	Unit	
Power Supply ($V_{CC} = 5.0 \text{ V}$)	VEE	-8.0 to 0	Vdc	
Power Supply ($V_{EE} = -5.2 \text{ V}$)	V _{CC}	0 to +7.0	Vdc	
Input Voltage (V _{CC} = 5.0 V) TTL	VI	0 to V _{CC}	Vdc	
Output Current — Continuous — Surge	l _{out}	50 100	mA	
Operating Temperature Range	т _А	0 to +75	°C	
Storage Temperature Range — Plastic — Ceramic	T _{stg}	–55 to +150 –55 to +165	°C	

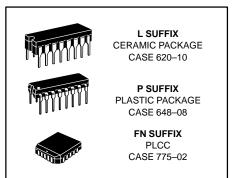
ELECTRICAL CHARACTERISTICS (V_{EE} = -5.2 V $\pm 5\%$, V_{CC} = 5.0 V $\pm 5.0\%$)

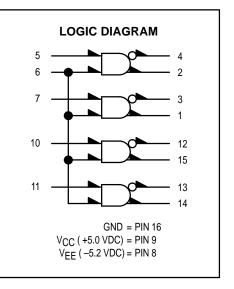
ymbol I _E I _{CCH}	0 Min —	Max 72	29 Min —	5° Max 66	7 Min —	75° Max 72	Unit mA
IE ICCH	Min —	72	Min —		Min —		
Іссн	_		—	66	-	72	mA
		40					
ICCL		16	_	16	—	18	mA
		25	_	25		25	mA
I _R		200 50		200 50		200 50	μA
١ _F		-12.8 -3.2		-12.8 -3.2		-12.8 -3.2	mA
(BR)in	5.5		5.5	-	5.5	-	Vdc
VI	_	-1.5	_	-1.5	_	-1.5	Vdc
V _{OH}	-1.02	-0.84	-0.98	-0.81	-0.92	-0.735	Vdc
V _{OL}	-1.95	-1.63	-1.95	-1.63	-1.95	-1.60	Vdc
V_{IH}	2.0	_	2.0	_	2.0	_	Vdc
V_{IL}	_	0.8	_	0.8	_	0.8	Vdc
	IR IF (BR)in VI VOH VOL VIH	^I R ^I F (BR)in 5.5 V _I V _{OH} V _{OL} V _{IH} 2.0	$\begin{array}{c c} I_{R} & - & 200 \\ - & 50 \\ I_{F} & - & -12.8 \\ - & -3.2 \\ (BR)in & 5.5 & - \\ V_{I} & - & -1.5 \\ V_{OH} & -1.02 & -0.84 \\ V_{OL} & -1.95 & -1.63 \\ V_{IH} & 2.0 & - \end{array}$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccc} I & I & I & I & I & I & I \\ \hline I R & - & 200 & - & 200 & - & \\ - & 50 & - & 50 & - & \\ I F & - & -12.8 & - & -12.8 & - & \\ - & -3.2 & - & -3.2 & - & \\ (BR)in & 5.5 & - & 5.5 & - & 5.5 \\ \hline V_I & - & -1.5 & - & -1.5 & - & \\ V_{OH} & -1.02 & -0.84 & -0.98 & -0.81 & -0.92 \\ \hline V_{OL} & -1.95 & -1.63 & -1.95 & -1.63 & -1.95 \\ \hline V_{IH} & 2.0 & - & 2.0 & - & 2.0 \end{array}$	$\begin{array}{c c c c c c c c c c c c c c c c c c c $

NOTE:

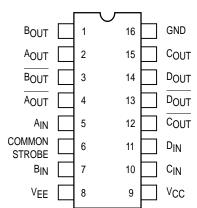
Each MECL 10H series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 lfpm is maintained. Outputs are terminated through a 50–ohm resistor to –2.0 volts.

MC10H124









Pin assignment is for Dual–in–Line Package. For PLCC pin assignment, see the Pin Conversion Tables on page 6–36 of the Motorola MECL Data Book (DL122/D).



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-	-						-	
		0 °		25 °		75 °		
Characteristic	Symbol	Min	Мах	Min	Мах	Min	Max	Unit
AC PARAMETERS								
Propagation Delay	^t pd	0.55	2.25	0.55	2.4	0.85	2.95	ns
Rise Time	t _r	0.5	1.5	0.5	1.6	0.5	1.7	ns
Fall Time	t _f	0.5	1.5	0.5	1.6	0.5	1.7	ns

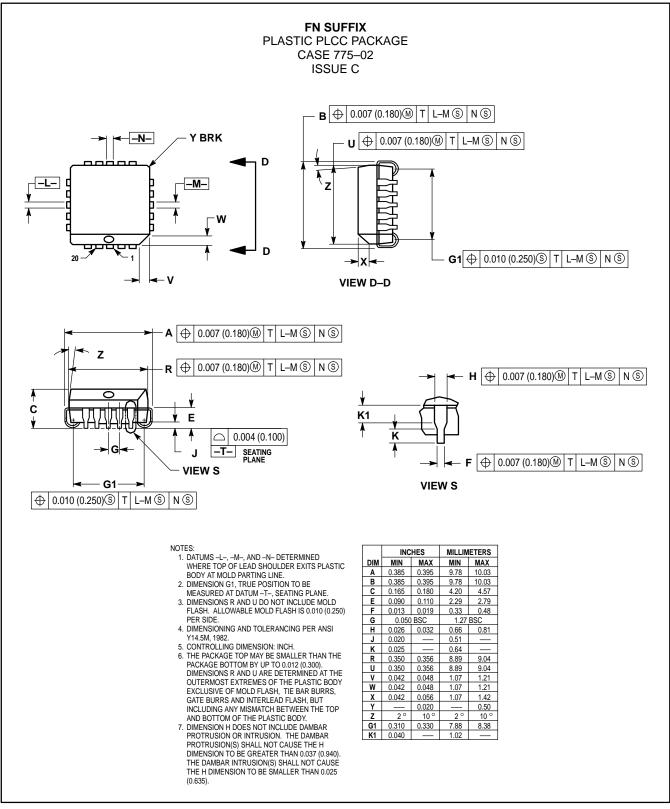
ELECTRICAL CHARACTERISTICS ($V_{EE} = -5.2 \text{ V} \pm 5\%$, $V_{CC} = 5.0 \text{ V} \pm 5.0\%$)

APPLICATIONS INFORMATION

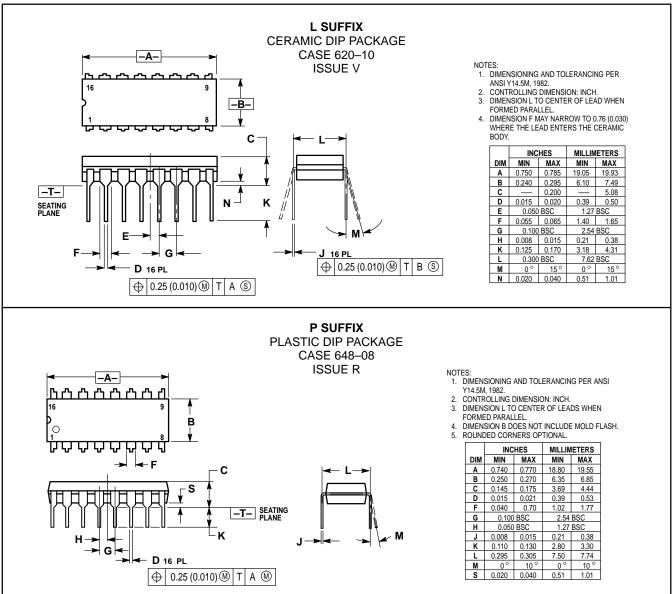
The MC10H124 has TTL–compatible inputs and MECL complementary open–emitter outputs that allow use as an inverting/non–inverting translator or as a differential line driver. When the common strobe input is at the low–logic level, it forces all true outputs to a MECL low–logic state and all inverting outputs to a MECL high–logic state.

An advantage of this device is that TTL-level information can be transmitted differentially, via balanced twisted pair lines, to MECL equipment, where the signal can be received by the MC10H115 or MC10H116 differential line receivers. The power supply requirements are ground, +5.0 volts, and -5.2 volts.

OUTLINE DIMENSIONS



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