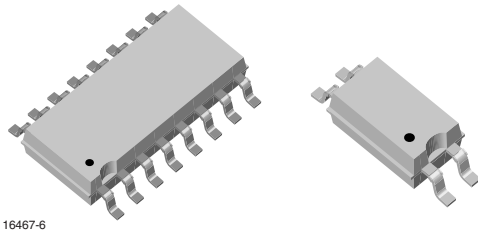
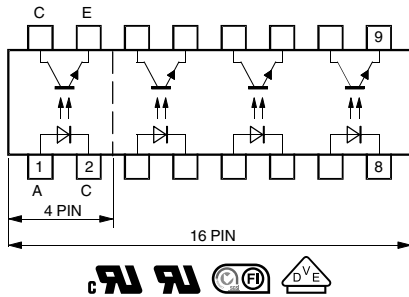




Optocoupler, Phototransistor Output, Single/Quad Channel, Half Pitch Mini-Flat Package



16467-6



FEATURES

- Low profile package (half pitch)
- AC isolation test voltage 3750 V_{RMS}
- Low coupling capacitance of typical 0.3 pF
- Current transfer ratio (CTR) selected into groups
- Low temperature coefficient of CTR
- Wide ambient temperature range
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC



RoHS COMPLIANT

APPLICATIONS

- Programmable logic controllers
- Modems
- Answering machines
- General applications

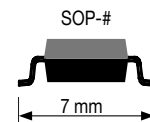
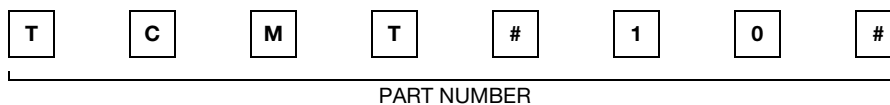
AGENCY APPROVALS

- UL1577, file no. E76222 system code M, double protection
- cUL CSA 22.2 bulletin 5A, double protection
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 (pending)
- FIMKO

DESCRIPTION

The TCMT1100 series consist of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4 pin (single channel) up to 16 pin (quad channel) package. The elements are mounted on one leadframe providing a fixed distance between input and output for highest safety requirements.

ORDERING INFORMATION



| AGENCY CERTIFIED/PACKAGE | CTR (%) | | | | | | | | | |
|--------------------------|---------------------------|----------|-----------|------------|------------|-----------|------------|-----------|------------|------------|
| | 5 mA | | 10 mA | | | | 5 mA | | | |
| UL, cUL, FIMKO | 50 to 600 | 40 to 80 | 63 to 125 | 100 to 200 | 160 to 320 | 50 to 150 | 100 to 300 | 80 to 160 | 130 to 260 | 200 to 400 |
| SOP-4, single channel | TCMT1100 | TCMT1101 | TCMT1102 | TCMT1103 | TCMT1104 | TCMT1105 | TCMT1106 | TCMT1107 | TCMT1108 | TCMT1109 |
| SOP-16, quad channel | TCMT4100T0 ⁽¹⁾ | - | - | - | - | - | TCMT4106 | - | - | - |

Notes

- Available only on tape and reel.
- ⁽¹⁾ Product is rotated 180° in tape and reel cavity.



| ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified) | | | | |
|--|--|------------|---------------|--------------------|
| PARAMETER | TEST CONDITION | SYMBOL | VALUE | UNIT |
| INPUT | | | | |
| Reverse voltage | | V_R | 6 | V |
| Forward current | | I_F | 60 | mA |
| Forward surge current | $t_p \leq 10\text{ }\mu\text{s}$ | I_{FSM} | 1.5 | A |
| Power dissipation | | P_{diss} | 100 | mW |
| Junction temperature | | T_j | 125 | $^{\circ}\text{C}$ |
| OUTPUT | | | | |
| Collector emitter voltage | | V_{CEO} | 70 | V |
| Emitter collector voltage | | V_{ECO} | 7 | V |
| Collector current | | I_C | 50 | mA |
| Collector peak current | $t_p/T = 0.5, t_p \leq 10\text{ ms}$ | I_{CM} | 100 | mA |
| Power dissipation | | P_{diss} | 150 | mW |
| Junction temperature | | T_j | 125 | $^{\circ}\text{C}$ |
| COUPLER | | | | |
| AC isolation test voltage (RMS) | Related to standard climate 23/50 DIN 50014 | V_{ISO} | 3750 | V_{RMS} |
| Total power dissipation | | P_{tot} | 250 | mW |
| Operating ambient temperature range | | T_{amb} | - 40 to + 100 | $^{\circ}\text{C}$ |
| Storage temperature range | | T_{stg} | - 40 to + 125 | $^{\circ}\text{C}$ |
| Soldering temperature ⁽¹⁾ | | T_{slid} | 260 | $^{\circ}\text{C}$ |

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.
- Wave soldering three cycles are allowed. Also refer to "Assembly Instructions" (www.vishay.com/doc?80054).

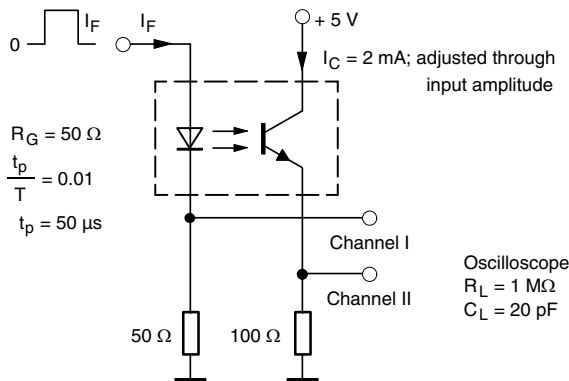
| ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified) | | | | | | |
|--|---|-------------|------|------|------|------|
| PARAMETER | TEST CONDITION | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| INPUT | | | | | | |
| Forward voltage | $I_F = 50\text{ mA}$ | V_F | | 1.25 | 1.6 | V |
| Junction capacitance | $V_R = 0, f = 1\text{ MHz}$ | C_j | | 50 | | pF |
| OUTPUT | | | | | | |
| Collector emitter voltage | $I_C = 100\text{ }\mu\text{A}$ | V_{CEO} | 70 | | | V |
| Emitter collector voltage | $I_E = 100\text{ }\mu\text{A}$ | V_{ECO} | 7 | | | V |
| Collector dark current | $V_{CE} = 20\text{ V}, I_F = 0\text{ A}$ | I_{CEO} | | | 100 | nA |
| COUPLER | | | | | | |
| Collector emitter saturation voltage | $I_F = 10\text{ mA}, I_C = 1\text{ mA}$ | V_{CEsat} | | | 0.3 | V |
| Cut-off frequency | $V_{CE} = 5\text{ V}, I_F = 10\text{ mA},$ $R_L = 100\text{ }\Omega$ | f_c | | 100 | | kHz |
| Coupling capacitance | $f = 1\text{ MHz}$ | C_k | | 0.3 | | pF |

Note

- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

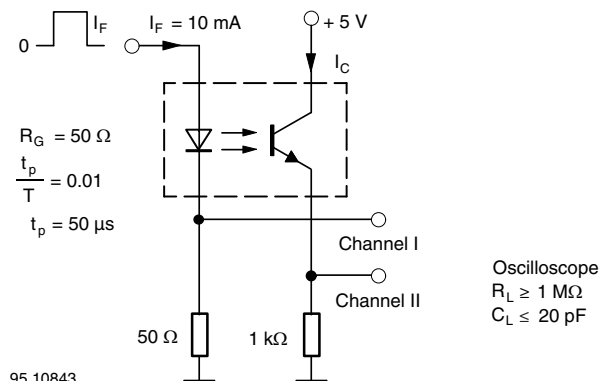
| CURRENT TRANSFER RATIO ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified) | | | | | | | |
|---|---|----------|--------|------|------|------|------|
| PARAMETER | TEST CONDITION | PART | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| I_C/I_F | $V_{CE} = 5\text{ V}, I_F = 5\text{ mA}$ | TCMT1100 | CTR | 50 | | 600 | % |
| | | TCMT1101 | CTR | 40 | | 80 | % |
| | $V_{CE} = 5\text{ V}, I_F = 10\text{ mA}$ | TCMT1102 | CTR | 63 | | 125 | % |
| | | TCMT1103 | CTR | 100 | | 200 | % |
| | | TCMT1104 | CTR | 160 | | 320 | % |
| | | TCMT1105 | CTR | 50 | | 150 | % |
| | $V_{CE} = 5\text{ V}, I_F = 5\text{ mA}$ | TCMT1106 | CTR | 100 | | 300 | % |
| | | TCMT1107 | CTR | 80 | | 160 | % |
| | | TCMT1108 | CTR | 130 | | 260 | % |
| | | TCMT1109 | CTR | 200 | | 400 | % |
| | | TCMT4100 | CTR | 50 | | 600 | % |
| | | TCMT4106 | CTR | 100 | | 300 | % |

| SWITCHING CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified) | | | | | | |
|--|--|-----------|------|------|------|---------------|
| PARAMETER | TEST CONDITION | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| Delay time | $V_S = 5\text{ V}, I_C = 2\text{ mA}, R_L = 100\text{ }\Omega$, (see figure 1) | t_d | | 3 | | μs |
| Rise time | $V_S = 5\text{ V}, I_C = 2\text{ mA}, R_L = 100\text{ }\Omega$, (see figure 1) | t_r | | 3 | | μs |
| Fall time | $V_S = 5\text{ V}, I_C = 2\text{ mA}, R_L = 100\text{ }\Omega$, (see figure 1) | t_f | | 4.7 | | μs |
| Storage time | $V_S = 5\text{ V}, I_C = 2\text{ mA}, R_L = 100\text{ }\Omega$, (see figure 1) | t_s | | 0.3 | | μs |
| Turn-on time | $V_S = 5\text{ V}, I_C = 2\text{ mA}, R_L = 100\text{ }\Omega$, (see figure 1) | t_{on} | | 6 | | μs |
| Turn-off time | $V_S = 5\text{ V}, I_C = 2\text{ mA}, R_L = 100\text{ }\Omega$, (see figure 1) | t_{off} | | 5 | | μs |
| Turn-on time | $V_S = 5\text{ V}, I_F = 10\text{ mA}, R_L = 1\text{ k}\Omega$, (see figure 2) | t_{on} | | 9 | | μs |
| Turn-off time | $V_S = 5\text{ V}, I_F = 10\text{ mA}, R_L = 1\text{ k}\Omega$, (see figure 2) | t_{off} | | 18 | | μs |



95 10804

Fig. 1 - Test Circuit, Non-Saturated Operation



95 10843

Fig. 2 - Test Circuit, Saturated Operation

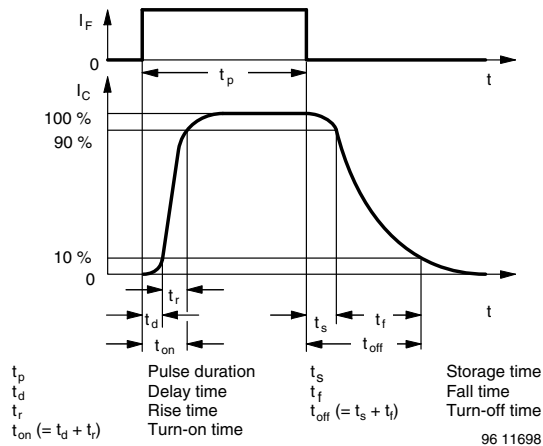


Fig. 3 - Switching Times

| SAFETY AND INSULATION RATINGS | | | | | | |
|--|-----------------------|--------|------|-----------|------|------|
| PARAMETER | TEST CONDITION | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| Climatic classification | IEC 68 part 1 | | | 40/110/21 | | |
| Comparative tracking index | | CTI | 175 | | 399 | |
| V _{IOTM} | | | 6000 | | | V |
| V _{IORM} | | | 707 | | | V |
| P _{SO} | | | | | 265 | mW |
| I _{SI} | | | | | 130 | mA |
| T _{SI} | | | | | 150 | °C |
| Creepage distance | | | 5 | | | mm |
| Clearance distance | | | 5 | | | mm |
| Insulation thickness, reinforced rated | per IEC60950 2.10.5.1 | | 0.4 | | | mm |

Note

- As per IEC 60747-5-2, § 7.4.3.8.1, this optocoupler is suitable for “safe electrical insulation” only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

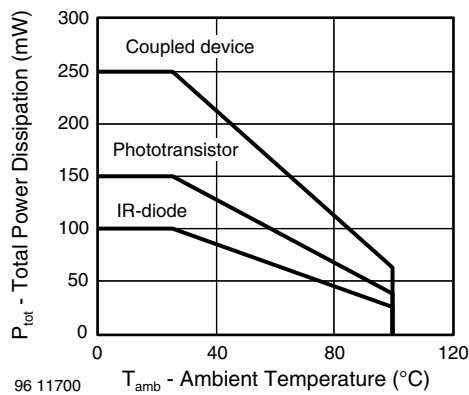


Fig. 4 - Total Power Dissipation vs. Ambient Temperature

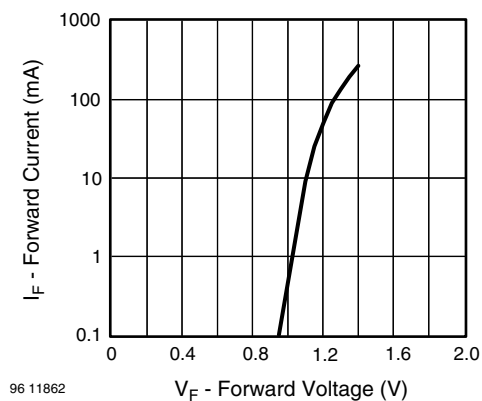


Fig. 5 - Forward Current vs. Forward Voltage

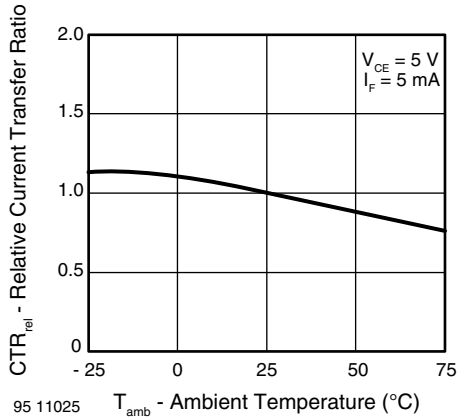


Fig. 6 - Relative Current Transfer Ratio vs. Ambient Temperature

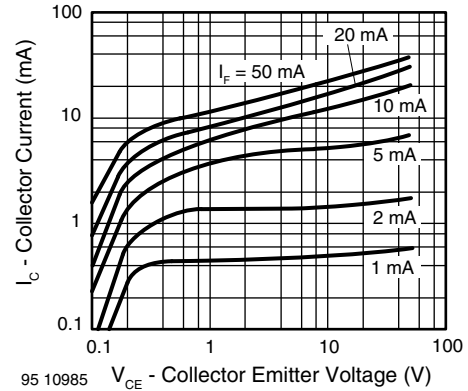


Fig. 9 - Collector Current vs. Collector Emitter Voltage

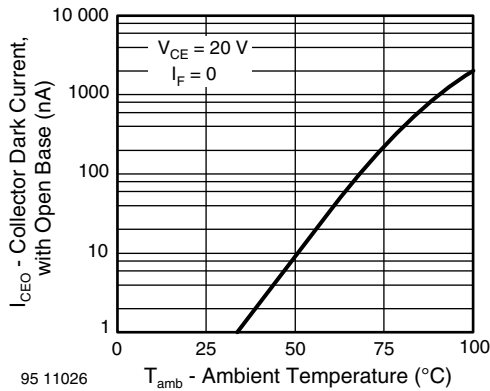


Fig. 7 - Collector Dark Current vs. Ambient Temperature

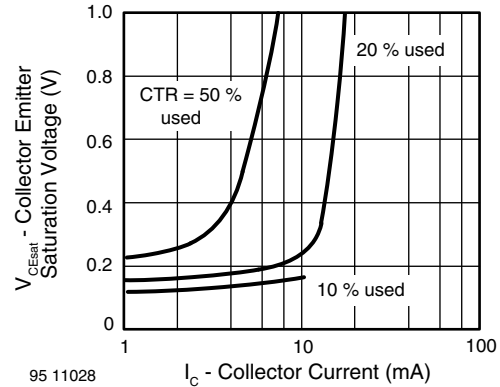


Fig. 10 - Collector Emitter Saturation Voltage vs. Collector Current

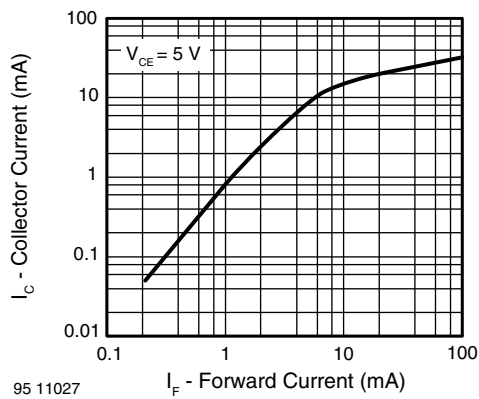


Fig. 8 - Collector Current vs. Forward Current

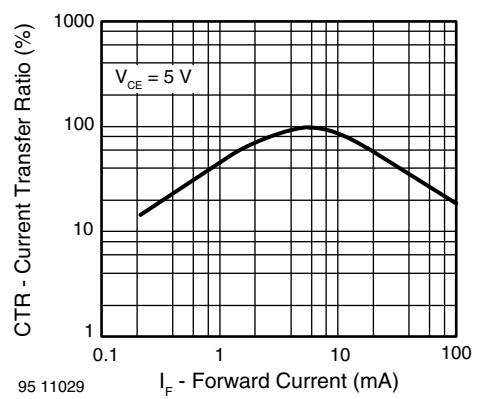


Fig. 11 - Current Transfer Ratio vs. Forward Current

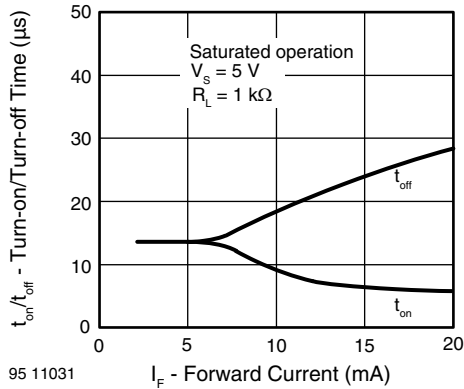


Fig. 12 - Turn-on/off Time vs. Forward Current

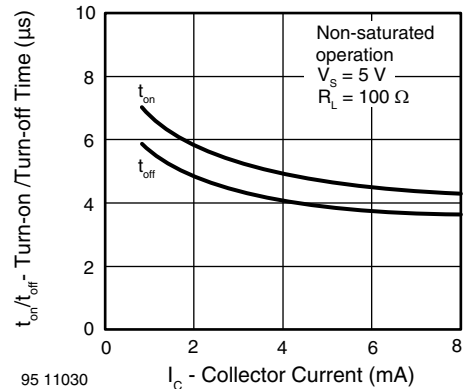
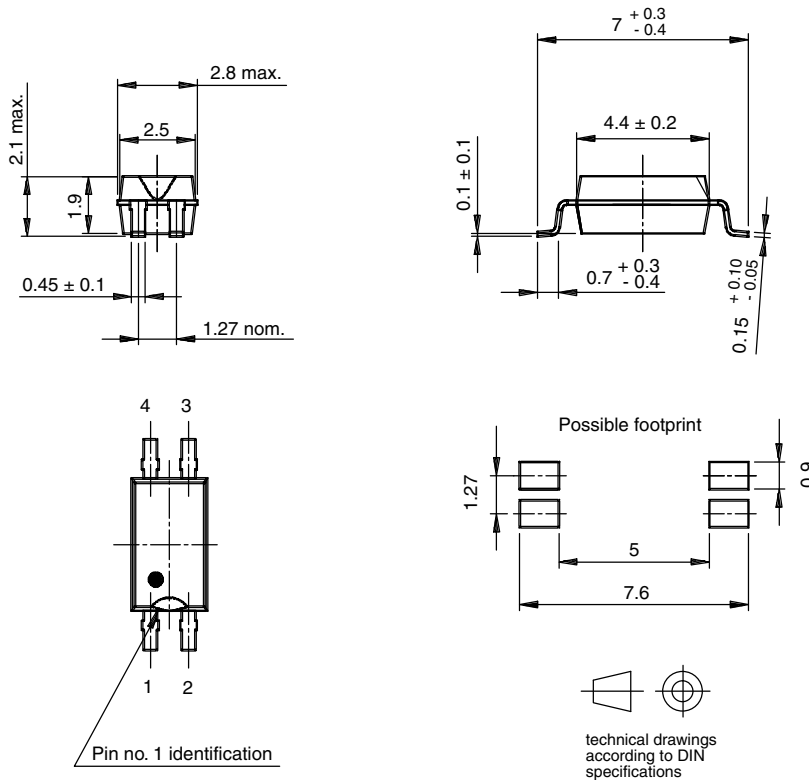
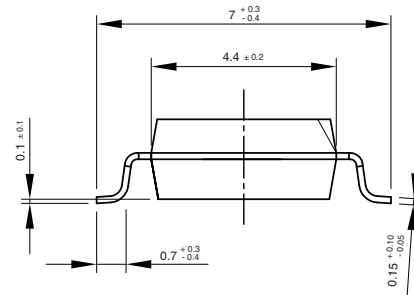
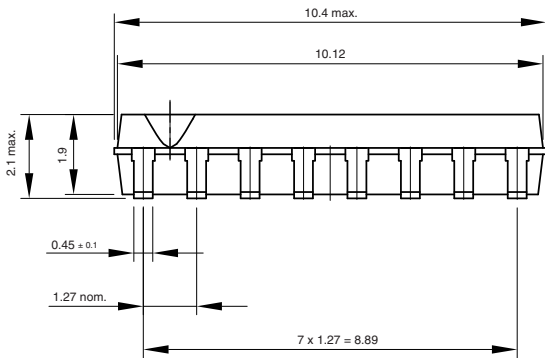


Fig. 13 - Turn-on/off Time vs. Collector Current

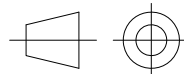
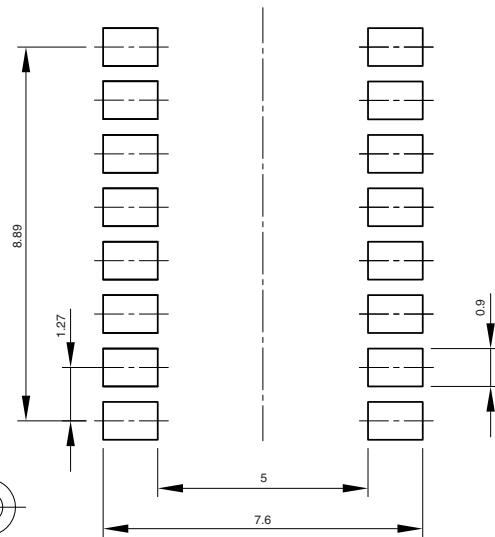
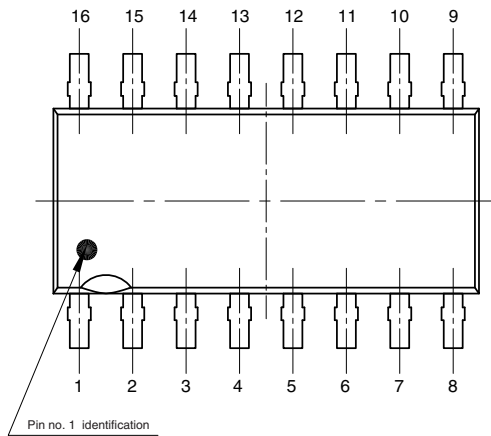
PACKAGE DIMENSIONS in millimeters



16283



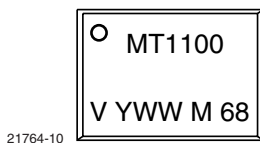
Possible footprint



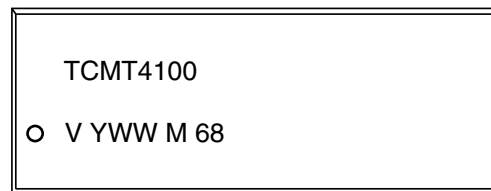
technical drawings according to DIN specifications

Drawing-No.: 6.544-5330.03-4
 Issue: 1; 04.04.00
 15226

PACKAGE MARKING (example)



21764-11





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